

Refine Search

Search Results -

Terms	Documents
(712/227).ccls.	404

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

Search History

 DATE: Sunday, January 09, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L21</u>	712/227.ccls.	404	<u>L21</u>
<u>L20</u>	714/28,43.ccls.	568	<u>L20</u>
<u>L19</u>	717/124.ccls.	276	<u>L19</u>
<u>L18</u>	L17 and (self-test or self test\$)	12	<u>L18</u>
<u>L17</u>	(on-board) near4 memory same test\$ same microprocessor\$	32	<u>L17</u>
<u>L16</u>	L15 and ((device under test) or dut)	1	<u>L16</u>
<u>L15</u>	L14 and microprocesso\$ and test\$	163	<u>L15</u>
<u>L14</u>	stor\$ near3 (on-board) near4 memory	593	<u>L14</u>
<u>L13</u>	(on-board) near4 memory	2411	<u>L13</u>
<u>L12</u>	l10 and (device under test) and microprocessor\$	1	<u>L12</u>
<u>L11</u>	L10 and ((device under test) or dut) near6 microprocessor\$	1	<u>L11</u>
<u>L10</u>	4622669.pn.	1	<u>L10</u>
<u>L9</u>	L8 and l7	0	<u>L9</u>
<u>L8</u>	717/124.ccls.	276	<u>L8</u>

<u>L7</u>	((device under test) or dut) near6 microprocessor\$	83	<u>L7</u>
<u>L6</u>	l1 and (processor\$ or micor\$)	1	<u>L6</u>
<u>L5</u>	l4 and (in-memory near9 test\$)	1	<u>L5</u>
<u>L4</u>	L3 and (complex\$ or compound\$ or complicat\$)	1	<u>L4</u>
<u>L3</u>	l1 and device\$	1	<u>L3</u>
<u>L2</u>	L1 and (memory\$ or coplex\$ or complicat\$)	1	<u>L2</u>
<u>L1</u>	5475843.pn.	1	<u>L1</u>

END OF SEARCH HISTORY